

Dr. Hayden SO Kwok-hay Assistant Professor Department of Electrical and Electronic Engineering, Faculty of Engineering The University of Hong Kong

Biography

Dr. Hayden So received his B.S. in Electrical Engineering and Computer Sciences (1998), his M.S. (2000) and his PhD (2007) from the University of California, Berkeley. He joined the University of Hong Kong in 2007 where he currently serves as an Assistant Professor.

His research has appeared in publications including ACM Transactions on Embedded Computing Systems (TECS), IEEE Micro, ACM Computer Architecture News, International Journal of Circuit Theory and Applications (IJCTA), and IEEE Transactions on Consumer Electronics.



Before joining the department in 2007, he was a member of the Berkeley Wireless Research Center where he developed BORPH, an operating system for the BEE2 FPGA platform. While working as an intern at Xilinx research lab, he developed a virtual file system for FPGA dynamic reconfiguration.

He is the recipient of the Croucher Innovation Award in 2013 for his work in "Power-Efficient High-Performance Heterogeneous Computer Systems". Hayden serves on the technical program committee of various international conferences and is the program Co-Chair of the 2014 International Conference on Field-Programmable Technology (FPT) and the Fifth International Symposium on Highly-Efficient Accelerators and Reconfigurable Technologies (HEART).

His main research interest is on the design and application of heterogeneous reconfigurable computers in the domain of biomedical imaging and big data processing. He is particularly interested in improving power-efficiency of computer systems through run-time reconfiguration and techniques that can bring reconfigurable computing to the main stream.

Awards

2013	Croucher Innovation Award, The Croucher Foundation
2012	Outstanding Teaching Award (Team), The University of Hong Kong
2011-12	Best Teacher Award, Faculty of Engineering, The University of Hong Kong